

REMARKS

Claim Rejections – 35 USC §102(e)

Claims 17-20 [sic] were rejected under 35 USC §102(e) as anticipated by Kwon U.S. Patent No. 6,124,840.

Please note that claims 19-20 were canceled in the prior Amendment A filed on December 9, 2003. Thus, only claims 17-18 stand rejected.

Rejection of claims 17-18 is respectfully traversed for the following reasons. As understood in the rejection, it is contended that Kwon teaches a gate signal line drive circuit having at least one tristate buffer per gate signal line and gate selection pulse change-over switches. Fig. 12 and col. 7, lines 18-23 of Kwon are cited in support of this contention.

It is respectfully submitted that Kwon teaches at col. 7, lines 18-23 that each of the switches S1W-S1Wn of the first switching unit 40 and buffer BF_n in the gate driver 30 can be substituted for a tristate buffer as shown in Fig. 12. This does not mean that the gate signal line driver circuit has both a tristate buffer and a gate selection pulse change-over switch per a gate signal line such as shown, for example, in Fig. 14 of the present patent application. Claims 17-18 of the present application therefore patentably distinguish over the Kwon reference.

Claim Rejections – 35 USC§103(a)

Claim 1 was rejected under 35 USC §103(a) as obvious over Kwon in view of Kobayashi et al. US Patent 5,936,455. It is respectfully submitted that Kwon is not a pertinent reference for the reasons set forth above, and that Kobayashi et al. does not cure the short-comings of the Kwon reference. Claim 1 is thus allowable for the reasons expressed above relative to claims 17-18.

CONCLUSION

Claims 1 and 17-18 are directed to patentable subject matter. The Kwon reference has been misinterpreted for what it actually teaches. Reconsideration and removal of the rejections entered in the Final Office Action are respectfully solicited.

A check in the amount of \$110.00 is enclosed for the requested one-month extension of time to make the filing of this Response timely.

It is respectfully submitted that this patent application is in condition for allowance, and an early allowance is respectfully requested.

Respectfully submitted,



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